## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner:

Examiner: C. Lee

Art Unit:

Art Unit:

Not yet assigned

Not yet assigned

Attorney Docket No: P12759C

2825

In re Application of: Robert Chau et al.

Serial No.: 10/618,226

Filed: July 11, 2003

For: A METHOD FOR MAKING A SEMICONDUCTOR

> **DEVICE HAVING A** HIGH-K GATE DIELECTRIC

Which is a Continuation of Application:

Serial No.: 10/082,530

Filed: February 22, 2002

Commissioner for Patents

P.O. Box 1450 Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is being submitted under 37 C.F.R. §1.97(b). Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the references cited on that form. It is respectfully requested that the cited references be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,

Dated: September 2, 2003

Mark V. Seeley Reg. No. 32,299

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(37 C.F.R. § 1.8(a))

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INFORMATION DISCLOSURE					Application Number		10/618,226	
THE RIVIATION DISCLOSURE					Filing Date		July 11, 2003	
/ US	TAT	MENT BY	' APPL	ICANT	First Named Inventor:		Robert Chau et al.	
STATEMENT BY APPLICANT				Group Art Unit Examiner Name		Unknown Unknown		
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